

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10052244	FILING DATE 01/23/2002	CLASS 438	SUBCLASS	GAU 2812	EXAMINER
**APPLICANTS: Sasaki Katsuhito					
**CONTINUING DATA VERIFIED:					
** FOREIGN APPLICATIONS VERIFIED: JAPAN 250752/2001 08/21/2001					
PG-PUB		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed		<input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO	
35 USC 119 condition met		<input type="checkbox"/> yes <input type="checkbox"/> no		OKI 295	
Verified and Acknowledged Examiner's initials					
TITLE : Method of manufacturing lateral double-diffused metal oxide semiconductor device					

NOTICE OF ALLOWANCE MAILED		Assistant Examiner		CLAIMS ALLOWED	
				Total Claims	Print Claims for O.G.
ISSUE FEE		Primary Examiner		DRAWING	
Amount Due	Date Paid			Sheets Drwg.	Figs.Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE		Application Examiner	
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